PCT Application: PCT/CA2003/000909: Priority Date 25/June/2002 US 10/520,040: Clean Copies, 18 Nov 2007 Page 10 of 16

Please replace the previous Abstract with the Abstract provided below.

## Abstract

The DSP MSP invention provides an implementation of programmable algorithms for analyzing a very wide range of low and high frequency wave-forms.

The DSP MSP comprises a synchronous sequential processor (SSP) for real time capturing and processing of in-coming wave-form including a programmable computing unit (PCU) for controlling SSP operations and supporting adaptive signal analysis algorithms. The DSP MSP further comprises a Sequential Data Recovery from Multi Sampled Phase (SDR MSP) for a receiver of an optical wave-form.